




## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING FACULTY PROFILE

<b>NAME</b>		<b>Dr. GayathriMonicka Subarnan</b>				
<b>DESIGNATION</b>		<b>Professor</b>				
<b>DEPARTMENT</b>		<b>Electrical and Electronics Engineering</b>				
<b>MAIL ID</b>	<b>gayathri.eee@adhi.edu.in</b>		<b>CONTACT NO</b>	<b>9566185503</b>		
<b>EDUCATIONAL QUALIFICATION</b>						
<b>Category</b>	<b>Name of the degree</b>	<b>Specialization</b>	<b>Year of Passing</b>	<b>Name of the College</b>	<b>Name of the University</b>	
UG	B.E	EEE	2002	Annai Teresa College of Engineering	Madras University	
PG	M.Tech	Power Electronics and Drives	2007	Bharath Institute of Higher Education and Research	Bharath University	
PhD	Ph.D	Multilevel Inverter and Drives	2015	Jerusalem College of Engineering	Anna University	
<b>TEACHING EXPERIENCE</b>						
<b>Name of the College</b>	<b>Designation</b>	<b>Joining Date</b>	<b>Relieving Date</b>	<b>Experience</b>		
				<b>Years</b>	<b>Months</b>	<b>Days</b>
<b>TOTAL</b>				<b>14</b>	<b>6</b>	
<b>Areas of Interest/ Specialization</b>		<b>Multi Level Inverter, Drive and Renewable energy</b>				
<b>NO.OF PAPER PUBLISHED</b>		<b>18</b>				



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<b>Journal</b>	<b>18</b>
<p><b>Top 5 International Journals Published</b></p>	<ul style="list-style-type: none"> <li>➤ Gayathri Monicka, J.&amp; V.Jamuna “Optimal Switching Strategy of Level shifted carrier based PWM technique for Asymmetric Multilevel Inverter” International Journal of Engineering Science (ISSN: 0020-7225) ELSEVIER, vol.113.(Annexure-I)</li> <li>➤ Gayathri Monicka, J &amp; Jamuna, V 2015, ‘Hybrid Cascaded MLI topology using Ternary Voltage Progression Technique with Multicarrier Strategy’, Journal of Electrical Engineering &amp;Technology (JEET) vol.10,pp.1610- 1620,2015</li> <li>➤ Gayathri Monicka, J , Jamuna, V 2015, ‘Control of ternary voltage progression based cascaded multilevel inverter using classy split multicarrier pulse width modulation’, Turkish Journal of Electrical Engineering and Computer Sciences.(Annexure-I).</li> <li>➤ Gayathri Monicka, J &amp; Jamuna, V 2015, ‘Multi Carrier based Multilevel Inverter with Minimal Harmonic Distortion’ International Journal of Power Electronics and Drive System (IJPEDS), vol.42</li> <li>➤ Gayathri Monicka,J, Dwarakesh,C. Amutha Devi, “Renewable based Multicarrier PWM Topology for Symmetric MLI” International Journal of Engineering and Technology, Vol 8 No 6 Dec 2016- Jan 2017,PP 2902- 2911</li> </ul>
<b>Conference</b>	<b>INT:10 NC:02</b>



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<b>NO.OF WORKSHOP ATTENDED</b>	<b>25</b>
<b>NO.OF EVENT ORGANIZED</b>	<b>05</b>
<b>PROJECTS GUIDED</b>	<b>20</b>
❖ <b>UG Level</b>	<b>11</b>
❖ <b>PG Level</b>	<b>6</b>
❖ <b>PhD</b>	<b>3</b>
<b>Administrative Assignments:</b>	<b>IQAC Coordinator, Chief Mentor</b>
<b>Membership of Reputed Bodies: (District/State/National/International Level)</b>	ISTE - LM115263 IAENG – 175536
<b>Books and Book Chapters:</b>	Book chapter accepted (Elsevier) proposal “Hybrid-Renewable Energy Systems In Micro grids: Developments And Control Of DG’s And Renewable Integration” ISBN 9780081024935. “Electronic Test Engineering” Chess and Owl Publishing House (In press)